

Fig.1

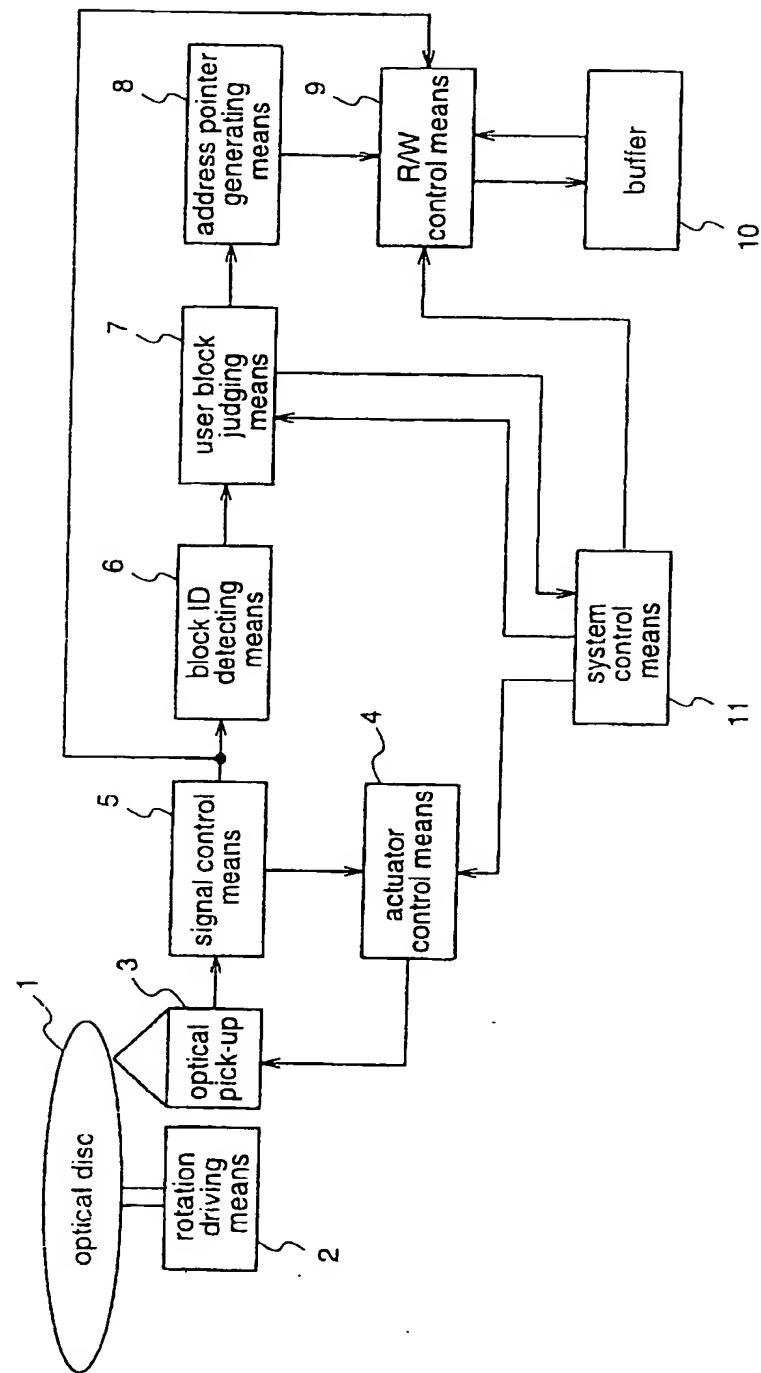


Fig.2(a)

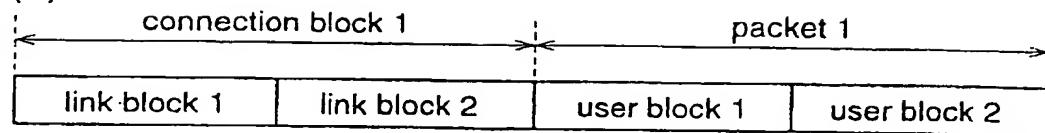


Fig.2(b)

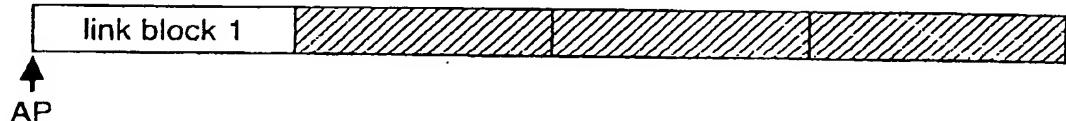


Fig.2(c)

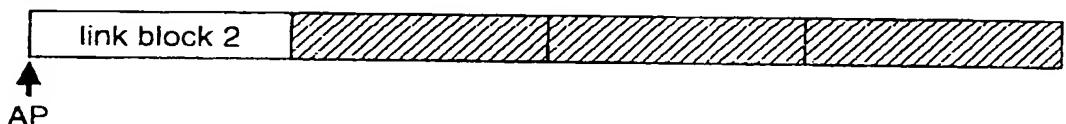


Fig.2(d)

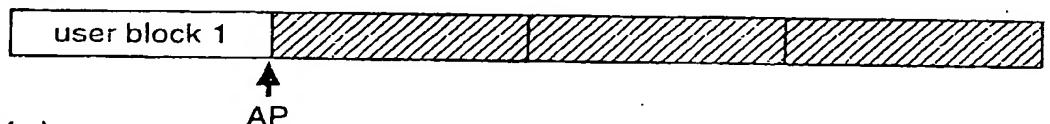
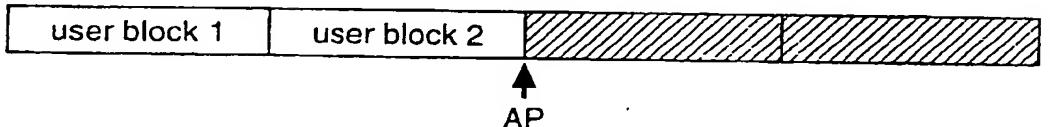


Fig.2(e)



AP:the position of the address pointer

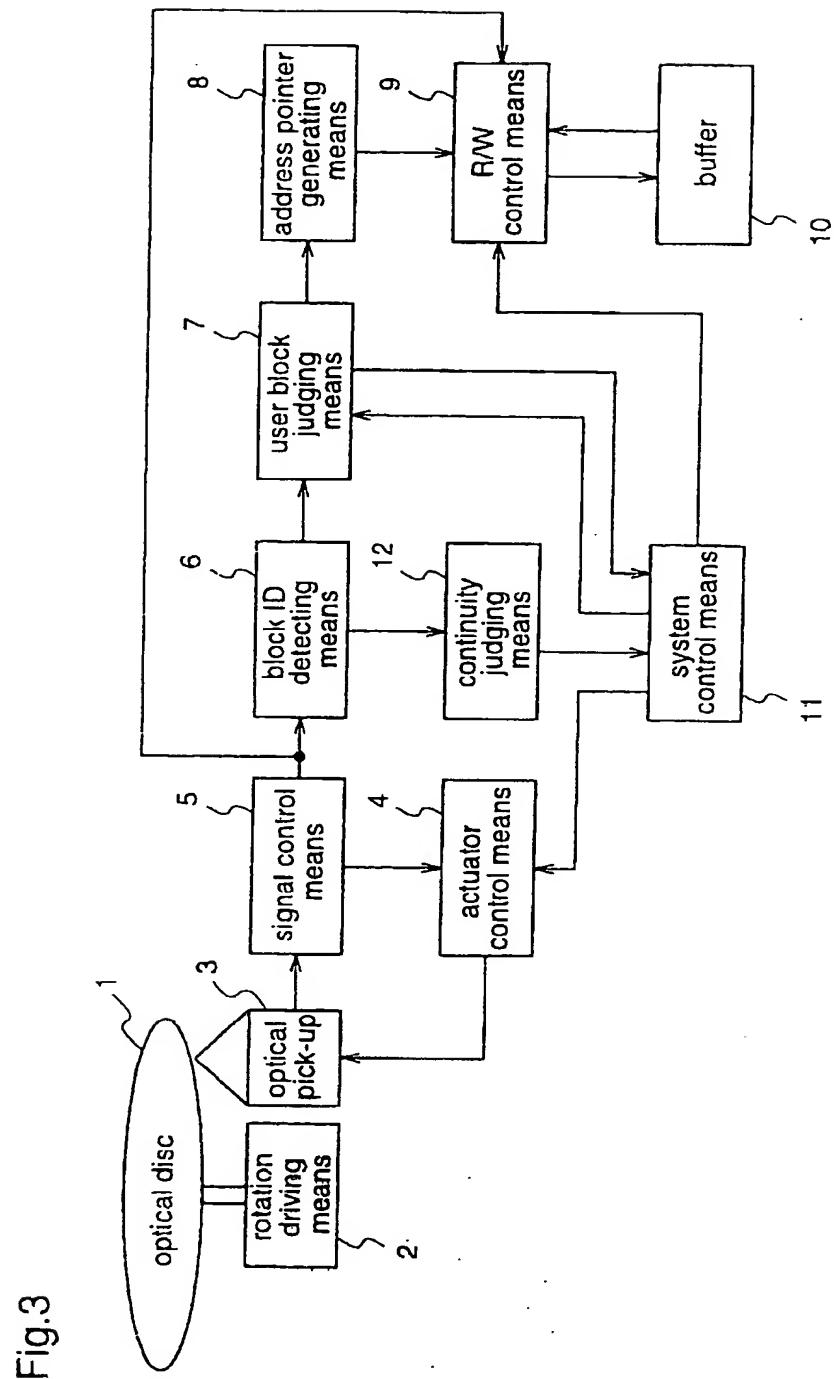


Fig.3

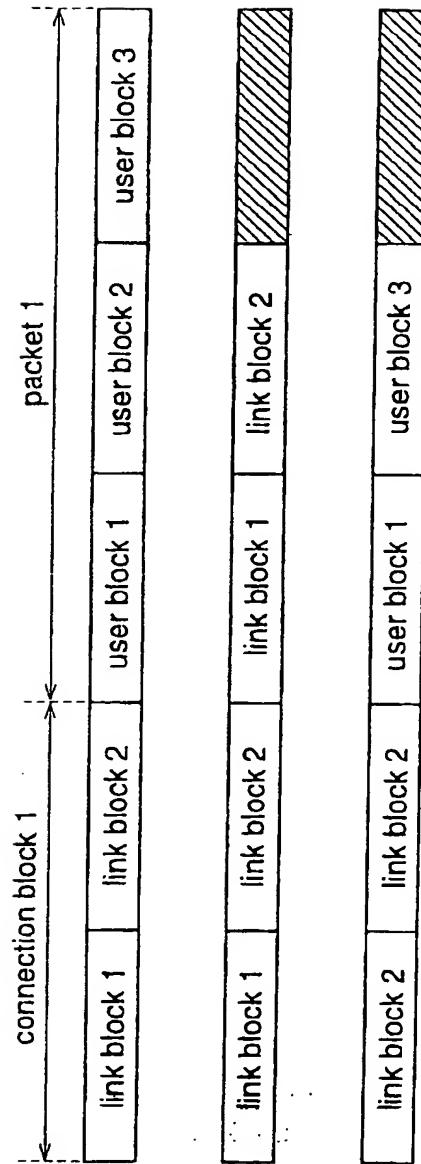


Fig.4(a)

Fig.4(b)

Fig.4(c)

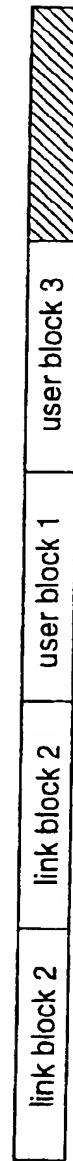


Fig.5

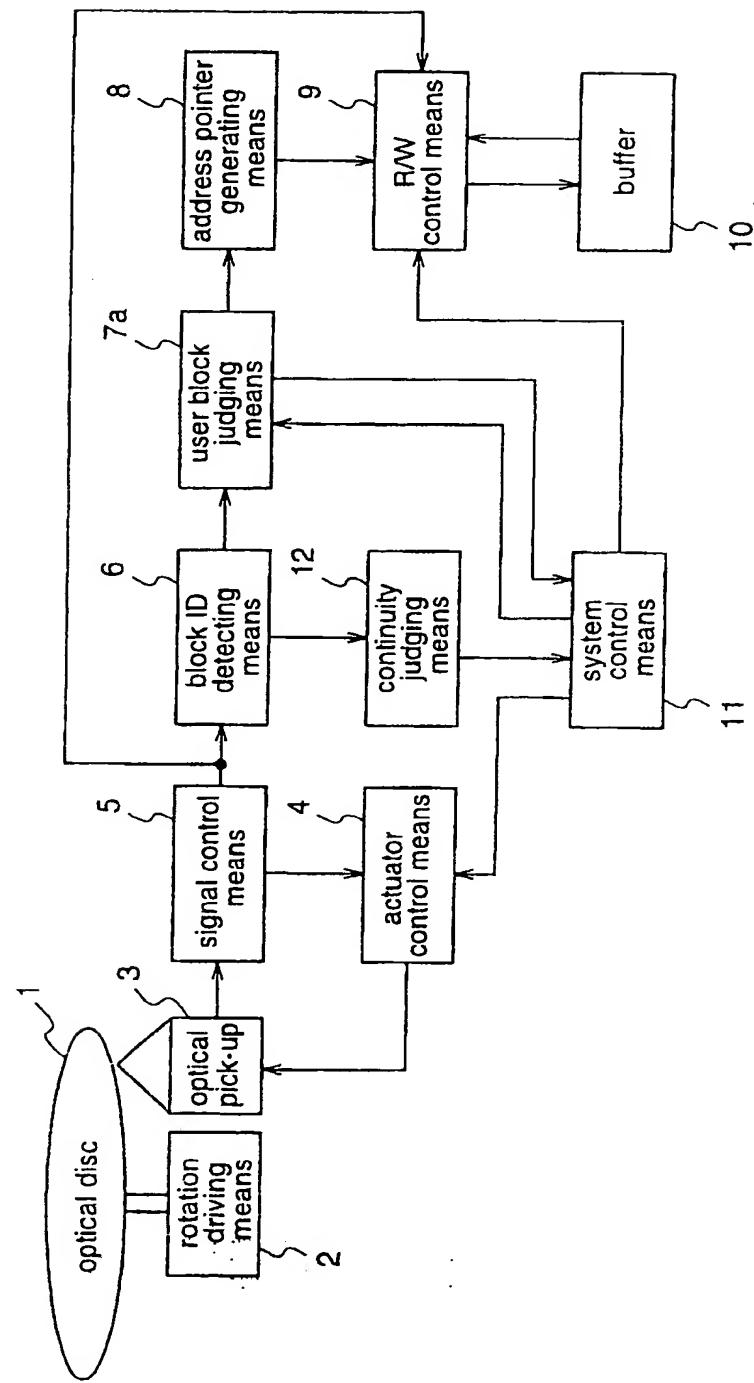
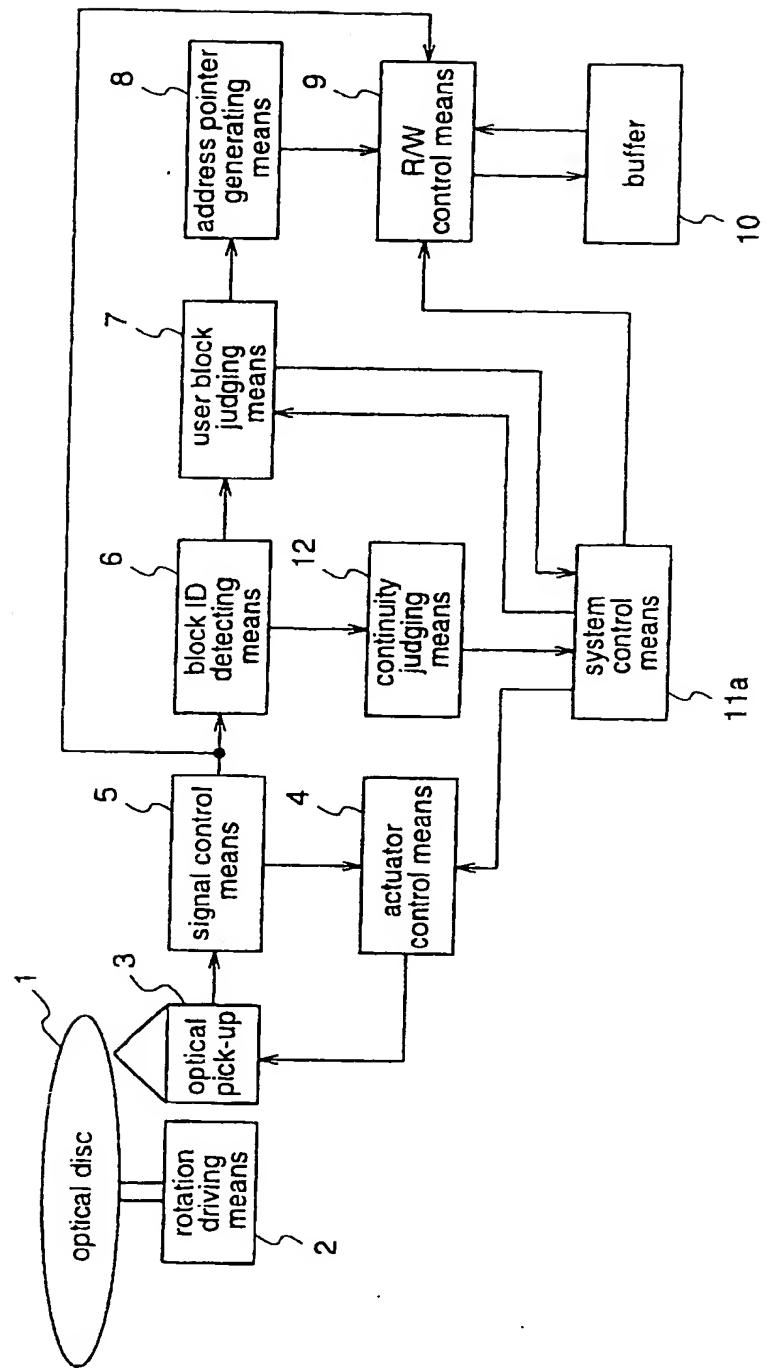
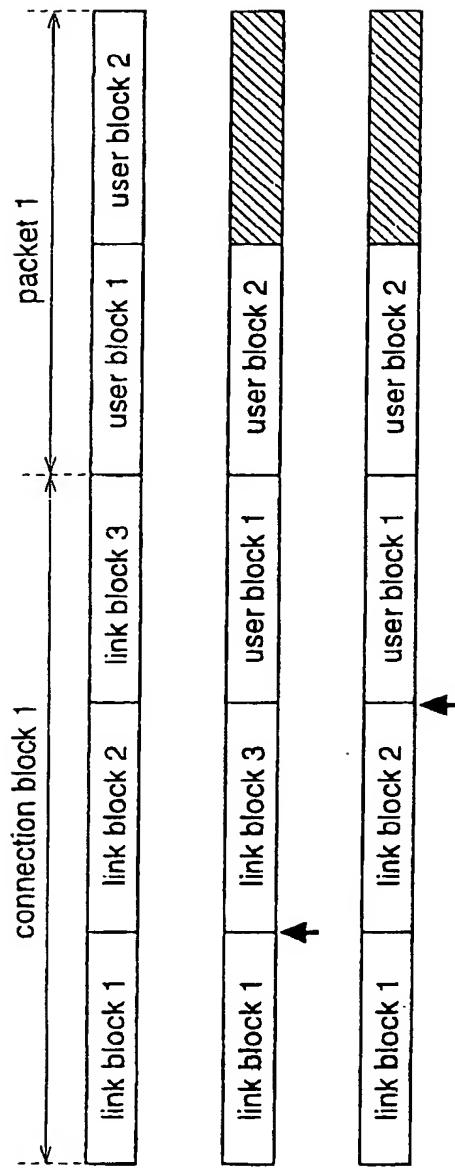


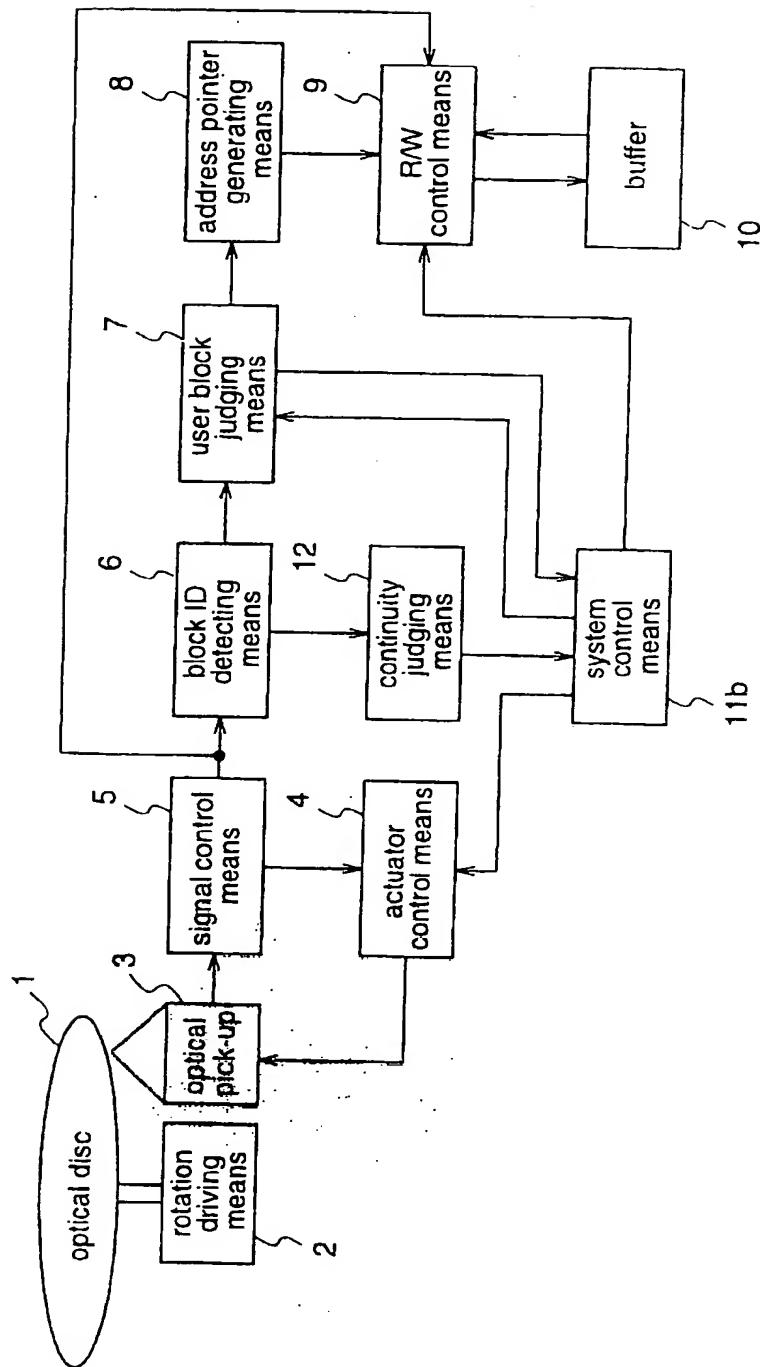
Fig.6

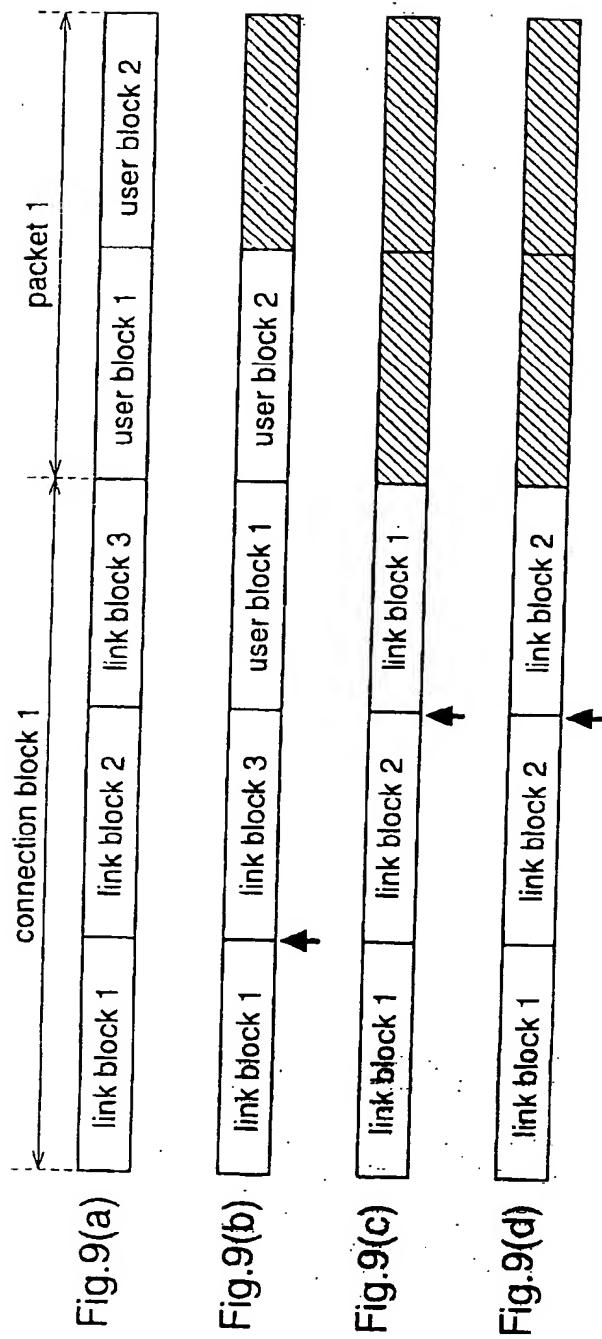




arrows : the position where the discontinuity  
of blocks has arisen

Fig.8





arrows : the position where the discontinuity  
of blocks has arisen

Fig.10

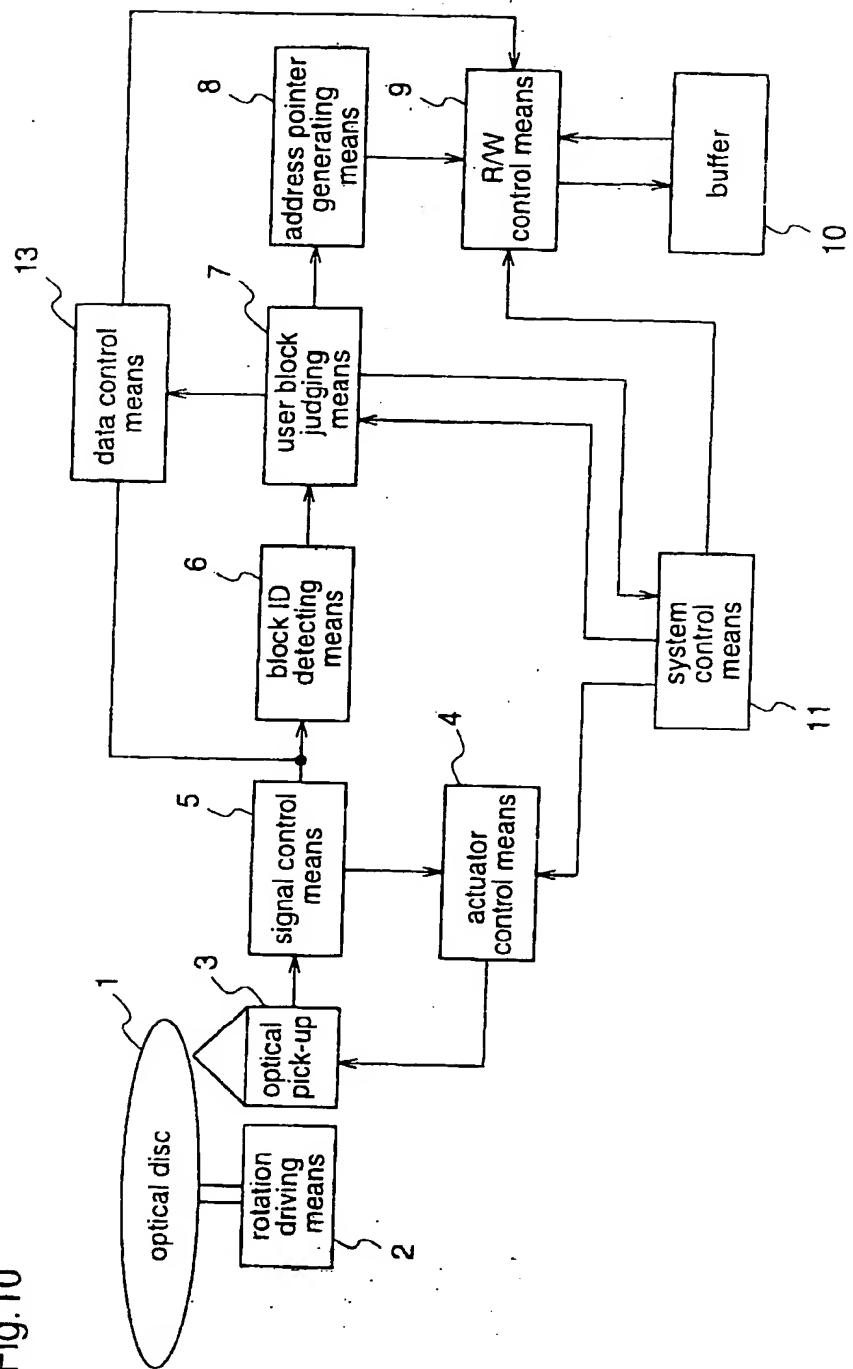


Fig. 11(a)

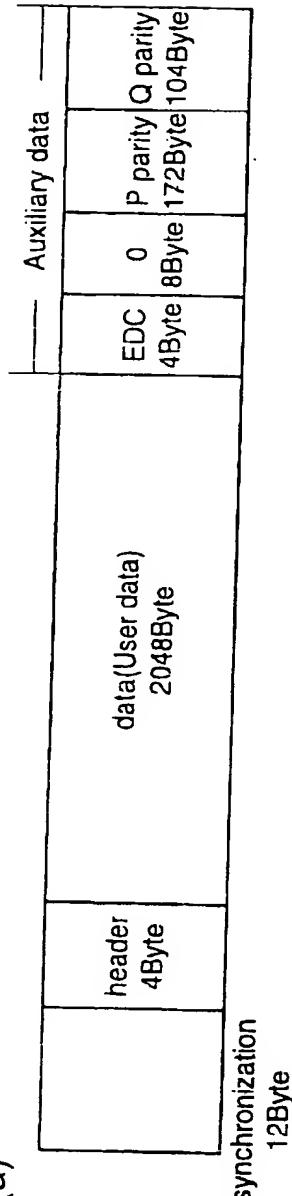


Fig. 11(b)

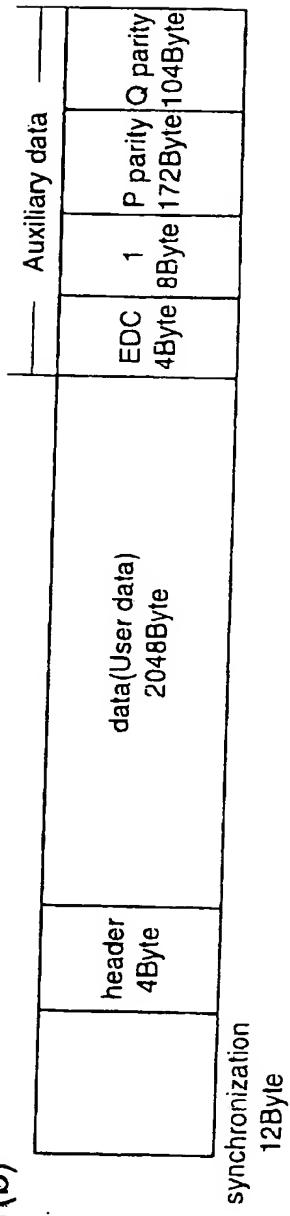


Fig.12(a)

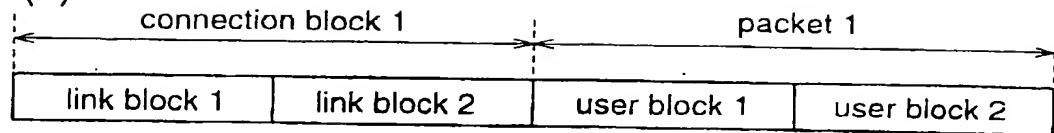


Fig.12(b)

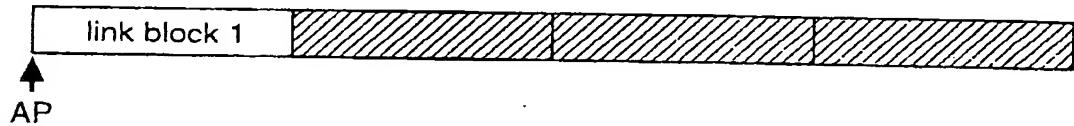


Fig.12(c)

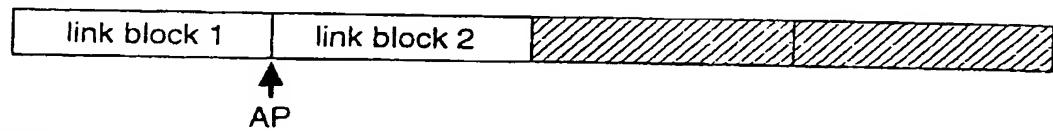


Fig.12(d)

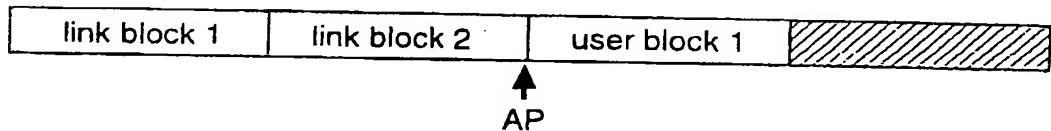
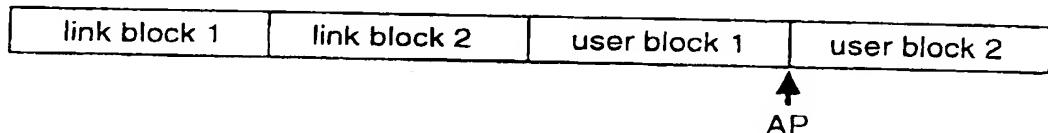


Fig.12(e)



AP:the position of the address pointer

Fig.13

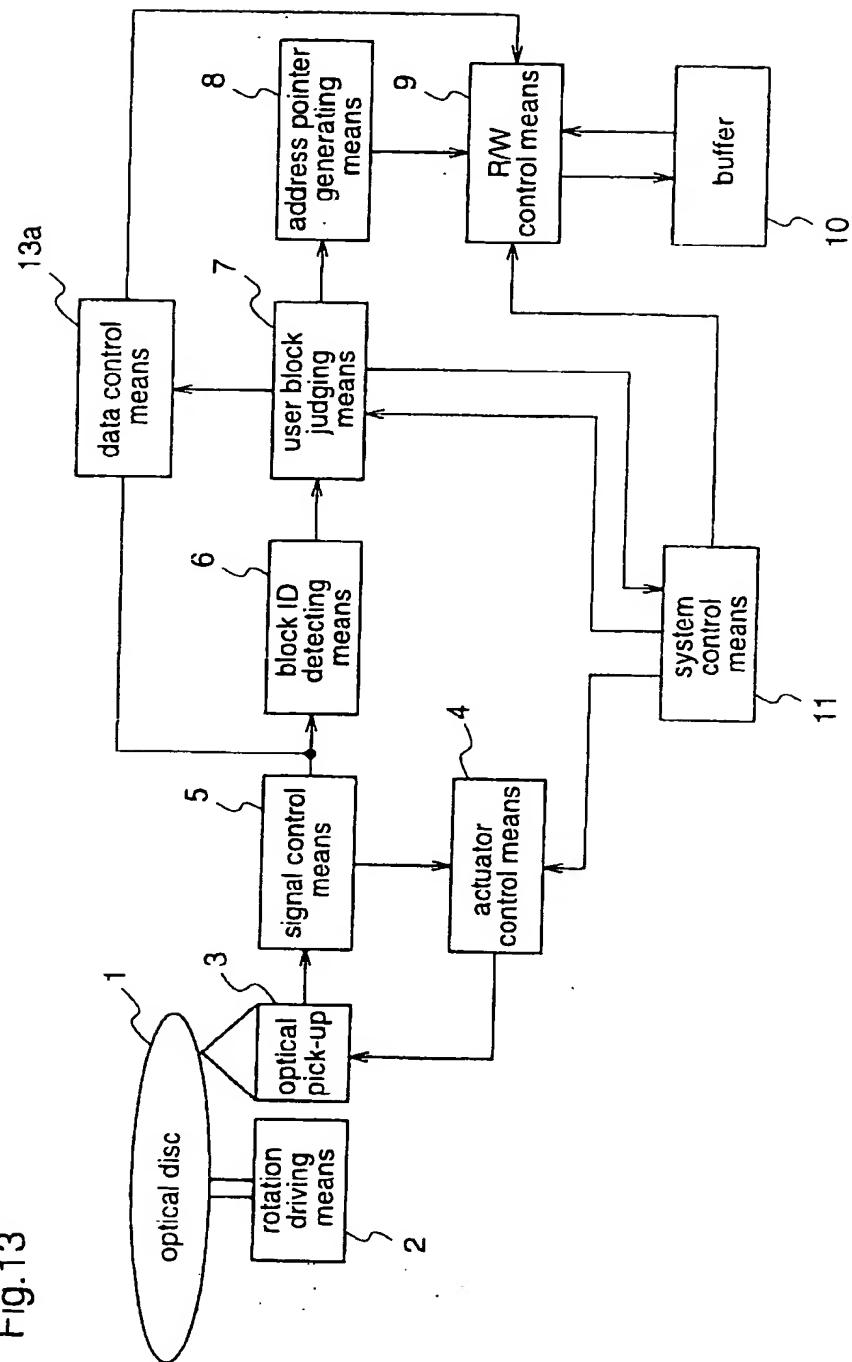


Fig. 14(a)

Fig. 14(b)

Auxiliary data	
header 4Byte	data(User data) 2048Byte
EDC 4Byte	0 8Byte P parity 172Byte Q parity 104Byte
synchronization 12Byte	

Fig.15(a)

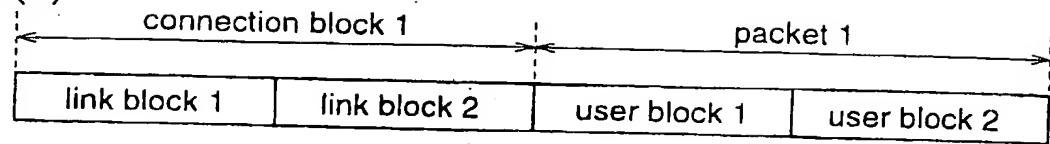


Fig.15(b)

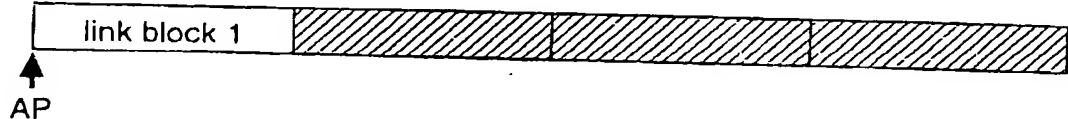


Fig.15(c)

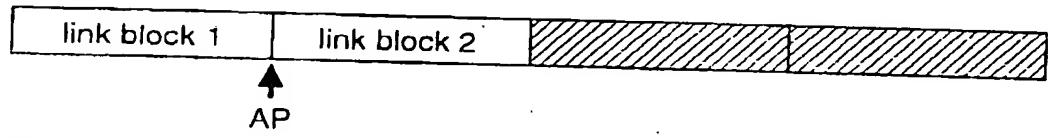


Fig.15(d)

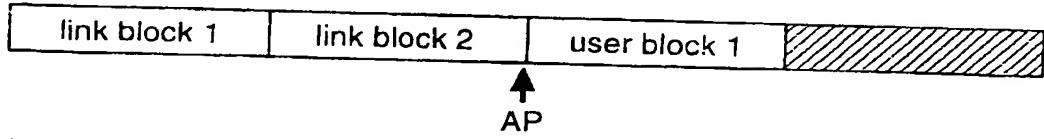
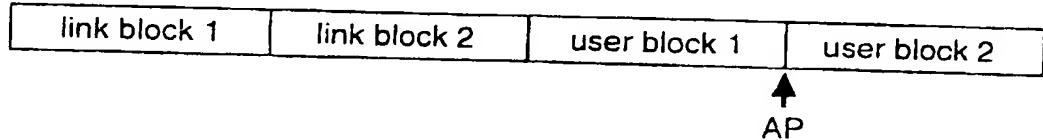


Fig.15(e)



AP:the position of the address pointer

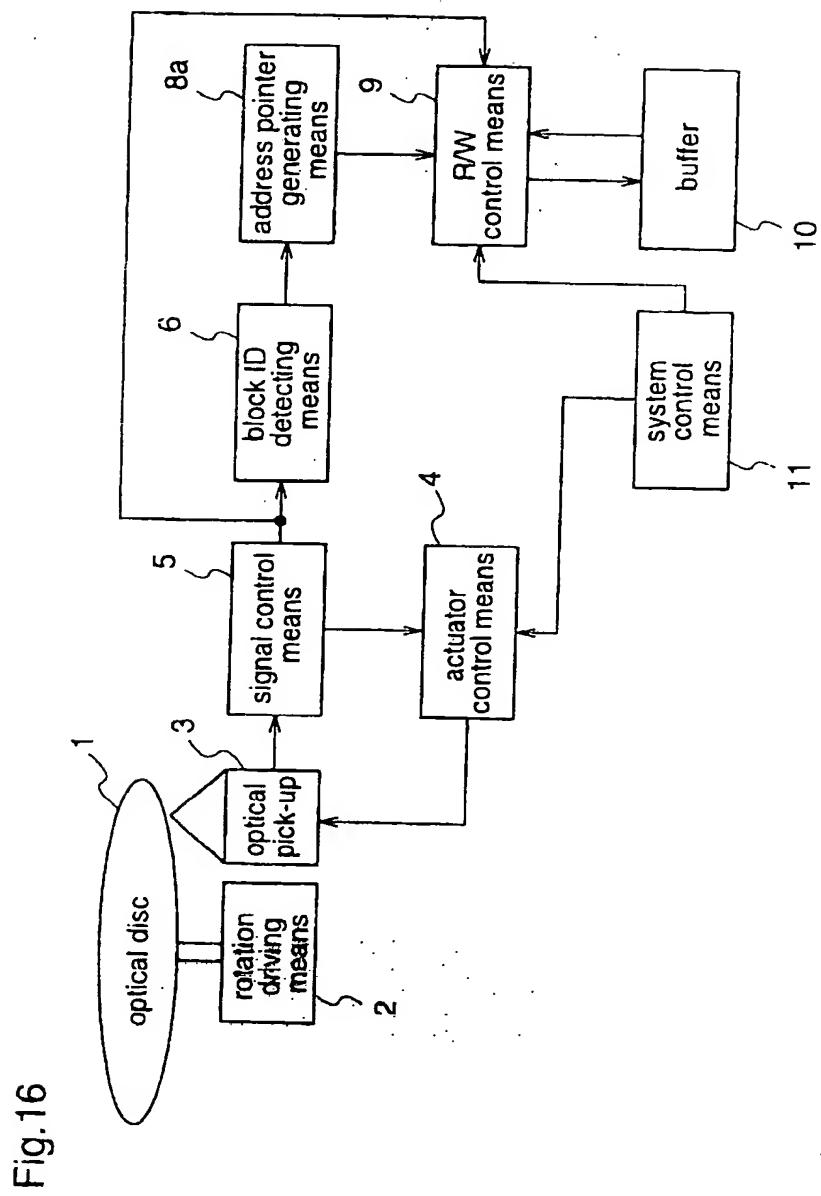


Fig.17(a)

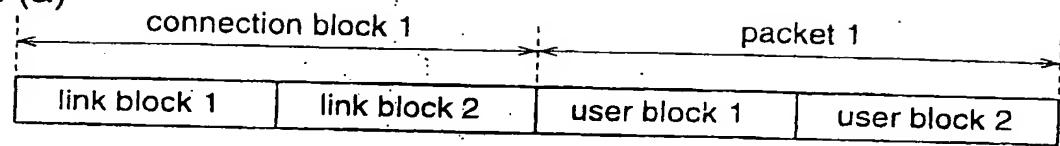


Fig.17(b)

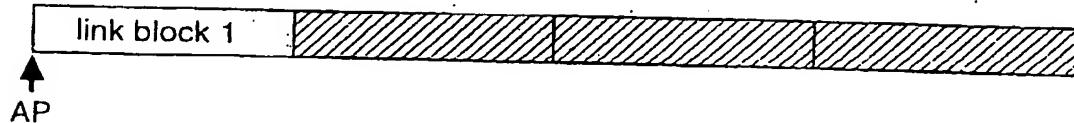


Fig.17(c)

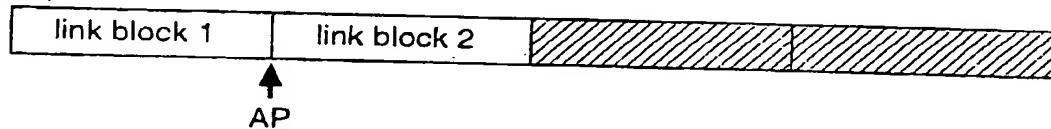


Fig.17(d)

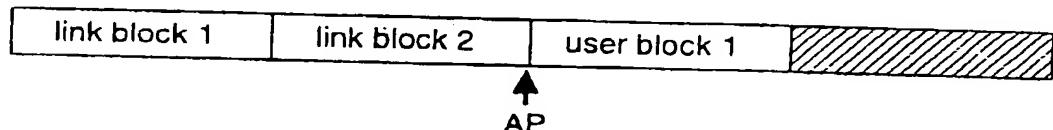
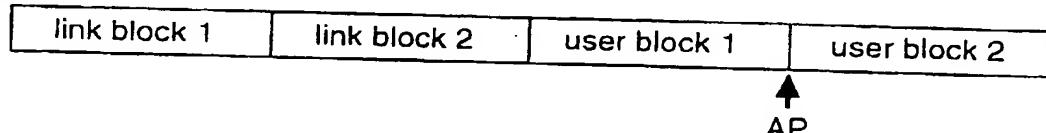


Fig.17(e)



AP:the position of the address pointer